

SYSTEM TERMINAL b SYSTEM TERMINAL a PROTECTION CIRCUIT FET52 FET51 🖺 Ċ GND SECONDARY T

FIG.3

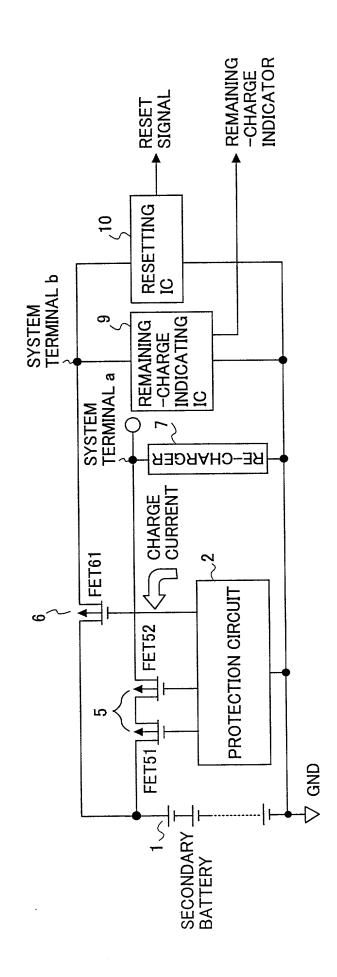


FIG.4

